

Abstract

A semiconductor memory device having a bank for storing a data and a port as a data I/O terminal includes a transmitter for delivering the data inputted from the port; a
5 global data bus for flowing an appearing current corresponding to the data outputted from the transmitter; and a receiver for sensing the appearing current by using a current-mirror and delivering the data corresponding to the sensed appearing
10 current into the bank, wherein a swing range of a data bus voltage in response to the appearing current is narrower than a gap between a supply voltage and a ground.